

PATENT
5201-25100/01-545

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of:
Boike et al.**

Serial No. 10/045,889

Filed: November 7, 2001

For: METHOD AND SYSTEM FOR A HOST PROCESSOR TO BROADCAST DATA TO INSTRUCTION OR DATA MEMORIES OF SEVERAL PROCESSORS IN A MULTI-PROCESSOR INTEGRATED CIRCUIT

Group Art Unit: 2182
Examiner: Harold J. Kim
Artv. Dkt. No. 01-545

Atty. Dkt. No.: 01-545

I hereby certify that this correspondence is being transmitted via facsimile or deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on the date indicated below:

June 28, 2004

Amela Denk
Parma, 8. 10. 1944

AMENDMENT; RESPONSE TO OFFICE ACTION MAILED MARCH 26, 2004

**Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313**

Dear Sir/Madam:

Responsive to the Office Action of March 26, 2004, please amend the case as follows:

Amendments to the Abstract begin on page 2 of this paper.

Remarks begin on page 3 of this paper.

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